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## **CALICE Test Beam 2022 Results and Outlook**

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**CEPC EU Workshop 2023** July 3-6, 2023

Affiliation:









## **International Large Detector**



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## International Large Detector (ILD)

- Multi-purpose  $4\pi$  detector designed for the ILC.
- Composed of multiple sub-detectors:
  - Vertex Detector (VTX)
  - Time Projection Chamber (TPC)
  - Electromagnetic Calorimeter (ECAL)
  - Hadronic Calorimeter (HCAL)
  - Muon Yoke
- Optimized for the application of **Particle Flow Algorithm (PFA)**





## **Particle Flow Algorithm**





## **Particle Flow Algorithm (PFA)**

- Jet composition
  - Photons: 27%
  - Charged Hadron: 63%
  - Neutral Hadron: 10%
- Energy depositions in the ECAL and HCAL are organized into topological clusters
- Requires highly granular calorimeters to differentiate particles.
- The ability to differentiate particles determines the jet energy resolution,  $\sigma$  /  ${\rm E}_{\rm iet}$











M.A.Thomson, 2009



## **Electromagnetic Calorimeter**



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## **PFA Requirements:**

- Jet energy resolution 3-4%
- Excellent photon-hadron separation

## **ECAL Requirements:**

- Extremely high granularity.
- Compact and hermetic (contained inside the magnetic coil)



All future e+e- collider projects feature at least one detector concept with this technology

Decision for CMS HGCAL based on CALICE/ILD prototypes 



## **Electromagnetic Calorimeter**



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## **Technical Implementation:**

- Layer radius: 1808 mm < R < 2028 mm
- Tungsten (W) as an absorber material
  - $\circ$  X<sub>0</sub> = 3.5mm
    - A small radiation length to make ECAL as compact as 24X<sub>0</sub> within 20cm
  - $\circ$  R<sub>M</sub> = 9mm
    - A small Molière radius to better separate nearby showers
  - $\circ \quad \lambda_{_{I}} = 96mm \rightarrow \lambda_{_{I}} \, / \, X_{_{0}} = 27.5$ 
    - Clearly distinguish electromagnetic showers from hadronic ones.
- Silicon (Si) as an active material
  - Pixels with high granularity
  - Excellent signal/noise ratio: 10 as design value



## All future e+e- collider projects feature at least one detector concept with this technology

• Decision for CMS HGCAL based on CALICE/ILD prototypes





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## Active Sensor Unit (ASU) Structure

- ASICs + PCB + Si wafer + Copper/Kapton = ASU
- Developed by IJCLab, CERN, Kyushu, OMEGA, LLR, SKKU
- Composition:
  - 4 Si wafer plates / 1 ASU (= 1 wafer / 4 chips)
  - 16 chips / 1 ASU
  - 64 pixels / 1 chip
- High voltage will be provided through SL board, developed by IJCLab



18 cm



## Silicon Tungsten Electromagnetic Calorimeter



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## ASIC SKIROC2(a)

- Silicon Kalorimeter Integrated Read-Out Chip
- Developed by OMEGA
- Wire bonded or in BGA package (IJCLab, LLR, Kyushu)



## **SL-Board**

- Digital readout unit
- Configure firmware
- Low voltage supply
- Developed by IJCLab

18 cm



## **PCB** Variants



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**FEV10-12** 



- ASICs in the BGA package
- Main "working houses" since 2014.

FEV\_COB



- Chip On Board (COB)
- ASICs wire bonded in cavities. •
- Thinner than FEV with BGA •
- External connectivity compatible with BGA based FEV10-12

**FEV13** 



- ASICs based on BGA packaging
- Different routing than FEV10-12
- Different external connectivity



## **Compact Readout**



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## Implementation in the tight space

- "Dead space free" granular calorimeters put tight demands on compactness
- Current developments in for SiW-ECAL stacks 15 layers
- The layer density is close to what is requested by the ILD. Rest of 15 layers (30 layers in total) will fit between the stack shown on the right.
- Core module takes care of complete readout including the software
- Software also monitors the <u>real-time beam status</u>.











#### Calibration



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## Commissioning

- Calibration constants
  - Pedestal values
    - 15 layers / 16 chips / 64 channels / 15 SCA
  - +1 MIP scaling factor
  - x2 gains
  - 15 x 16 x 64 x (15+1) x 2 = <u>491,520 parameters</u>
- Masking need to be done in order to silence the noisy cells.
- Multiple dry runs and cosmic ray runs were taken to filter out the defect cells.
- Detector signals come in form of electric pulses.
- DAC threshold determination were performed by injecting 1 MIP equivalent pulse and measuring the drop point for the hit counts.





## Trail of R&D



## Physics Prototype (2005 - 2010)

Proof of principle of granular calorimeters. Large scale combined beam tests.

#### **Technical Prototype (2010-)**

Engineering challenges High granularity, lower noise

#### 15 layer assembly (2021-)

Further technical challenges with more realistic scenario using 15 layers (½ of real life ILD ECAL)





### Test Beam 2022



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#### Test Beam March 2022 @ DESY

- Electron beam: 1-5 GeV
- First attempt conduct joint data taking with AHCAL group.
- Common data taking was performed.



#### Test Beam June 2022 @ CERN

- Electron, muon, pion beam: 10-200 GeV
- Original plan was to use few new stabs to conduct the synchronization.
- Recycling old ASUs  $\rightarrow$  15 layer
- Disassembled and reassembled the old slabs.
- Successful synchronization of data recorded with SiW-ECAL and AHCAL





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J. Kunath, F. Jimenez-Morales, SiW Ecal Analysis Meeting, 22/09/22



After proper filtering energy resolution in right ballpark for current prototype Convergence in agreement data/MC



## CERN Test Beam 2022-06



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#### SiW-ECAL

- Sensors
  - 15 layers
  - 16 chips / layer
  - 64 channel / chip
  - <u>15 x 16 x 64 = 15,360 cells</u>
- Tungstens: up to 21 X<sub>0</sub>
- Volume: 640 x 304 x 246 mm<sup>3</sup>
- Commissioned 2020-2022
  - $\circ$  4.5 x 10<sup>5</sup> calibration constants for one ASIC feedback capacitor setting.

## **CERN SPS Beam**

- Energies
  - e : 10, 20, 40, 60, 80, 100, 150 GeV
  - $\circ~~\mu~$  : 50, 150 GeV
  - $\circ$   $\pi$  : 10, 20, 70, 100, 150, 200 GeV





## Layer Setup



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Layer	SLAB	SLBoard- ID	ASU type	wafer (um)	W (mm)	X0 cumulative
0	34	15	FEV13 K1	650	4.2	1.2
1	37	28	FEV13 D	650	4.2	2.4
2	36	18	FEV13 A	650	4.2	3.6
3	38	12	FEV13 C	500	4.2	4.8
4	35	16	FEV13 B	650	4.2	6.0
5	39	25	FEV13	500	4.2	7.2
6	29	22	СОВ	500	4.2	8.4
7	30	19	FEV12	500	4.2	9.6
8	33	20	СОВ	500	4.2	10.8
9	31	17	FEV12	500	5.6	12.4
10	19	13	FEV11	320	5.6	14.0
11	18	2	FEV11	320	5.6	15.6
12	23	6	FEV10	320	5.6	17.2
13	40	27	FEV13	320	5.6	18.8
14	17	10	FEV11	320	5.6	20.4



#### ILC & ILD Setup

- ILC bunch trains come in frequency of 5 Hz. The time is synchronized between the beam and ILD.
- ILD will operate in **power pulsing mode**, where:
  - Electronics are switched on during > -1 ms of bunch train and data acquisition.
  - Bias currents being shut down between the bunch trains.
- No active cooling necessary.





## **Online Beam Monitoring**



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## **Temperature and Voltage Monitor**

- Displays relative temperature inside the detector
- Monitors AVDD in volts to check the detector

Efforts by Jihane & Dominique (IJCLab)

## **Beam Spot**

- Displays hit maps for each layer.
- Allow for real-time beam and detector tuning
- e.g. Adaquation of beam rates or thresholds Ο





### Correlation

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## **Clock Synchronization**

- Clocks between ECAL and AHCAL need to be synchronized in order to conduct a common data taking.
- 40 MHz external clock sent from the Clock and Control Card (CCC), operated by the common DAQ system, EUDAQ.
- For each readout, Bunch Crossing ID (**BCID**) is assigned with recorded time stamp.



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Fig. Difference between BCIDs in AHCAL and ECAL



## Particle Identification



101189

440.7

174.6

396.3

10

1

751.7 J2







## **Event Display**





Fig. SiWECAL Electron 40 GeV



Fig. AHCAL Pion 106 GeV



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## Hit Maps

- Hit distribution for the individual layer is mapped for one of the **10 GeV electron runs**, which was taken for <u>30 mins</u>.
- The beam is shot at (x, y) = (-42, -42) mm.
- The beam axis position agrees with the position we observed in the online beam monitoring.
- The large square corresponds to the size of single wafer pad. Later we observed some wafer delamination, possibly due to the degrading in the glue.
- Noisy cells, along with the cells behind the delaminated wafers were masked.















Hit XY e 10 GeV slab



HI XY & 10 GeV slab 8











Hit XY e' 10 GeV slab 13







## W

## **Hit Distribution**

- Number of hits deposited to the individual layer is shown for the same run.
- The distribution is normalized the number of events recorded. (24k events)
- Required minimum of 12 layer coincidences for every events. Event selection efficiency: 46.4 %.
- Hits with energy lower than 1 MIP was filtered.
- Shower maximum corresponds to slab 6 and 7.









Number of total hits at 10 GeV in slab 4 Number of total hits at 10 GeV in slab 5





Number of total hits at 10 GeV in slab 8







Number of total hits at 10 GeV in slab 12 Number of total hits at 10 GeV in slab 13







## **Hits per Layer**

- Layer numbers of the deposited hits were plotted.
- The distribution should roughly correspond to the shower profile of the electron beam at 10 GeV.
- Shower maximum observed around slab 5-7.
- Some non-continuity in the distribution (e.g. layer 4), is caused by masking and wafer delamination, which leads to inability for the hit registration.



#### Hits per layer at 10 GeV

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## **Total Hits**

- Total hit plot has excellent distribution with peak at 115 hits. .
- Short tail before the peak means there are small chances of missing . hits from the EM shower.
- The tail on the higher end mostly likely originated from the electrical . noises registered as hits, which can happen due to cross-talks between the circuits.

#### Energy

- Total hit energy is plotted as a unit of Minimum Ionization Energy (MIP) .
- Gaussian peak at 580 MIPs .
- Study using simulation is required to quantitatively understand the result. .



## Total Hit Energy at 10 GeV







- Results from the Test Beam June 2022 @ CERN were presented.
  - First successful operation using **15 layers** of ECAL, <u>together with AHCAL</u>.
    - Over 15,000 cells in total.
  - Clock synchronization with AHCAL data taking.
  - Difficulty in dealing with large amount of hits, especially at high energy.
  - Pilot study was performed with **10 GeV electron sample**.
- Missing hits
  - Some hits were missing from the data due to masking and wafer delamination.
  - Such effects gets involved at energies higher than 20 GeV.
- Simulation analysis
  - Simulation analysis is simultaneously performed to check the data quality.
  - The study is still undergoing.
- Detector Development
  - New PCBs for updated stack available. (Metrology and electrical tests currently conducted in-house)





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# Backup

Y. Okugawa - TB2022-06 CERN

## **Electronic Circuit Layout for the SKIROC 2**

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12 bit-ADC Ramp



ADC test Ch63. ------.... HOLD READ Sel ADC test? (slow control) Ch0 -Depth=15 C2=1.6pF  $\mathbf{A}$ -~~ Slow Sh. G1 TDC ramp R2=100k out\_adc signal Rf 🖌 I or 10M RI=4k C1=45pF Т READ m HOLD out\_tdc M Cf Time, vref ss U Depth=15 ssh GI, X 400f,800f, 1.6p, 3.2pF ssh\_G10  $\mathbf{A}$ out ssh G1 C2=3pF conversion -IF out ssh G10 Signal Slow sha w Slow Sh. G10 in PA R2=60k signal R1=22k C1=8pF PAC READ HOLD Gain\_selection -mil test Vth gs vref ss Depth=15 4 100f, 200f, 300f or 400fF Test Pulse TDC on? in calib (slow control) Fast Shaper Gain10 w 300k 5p,10p, 15p or 20pF HOLD Auto Gain ? Forced Gain ? (slow control) Mask, Trig\_Ext, Val\_Evt, † detector 8-bit Delay Box: 100ns to 300ns vref fs Vth\_trigger Sel FlagTDCb Ext ? Forced FlagTDCb ? 4-bit DAC (slow control) adjustment out\_trigger FLAG TDC 10-bit DAC 10-bit DAC (from Digital ASIC)

12 bit-TDC Ramp







## Hold Scan



- Signal is needed to be read along the pulse that is generated by the slow shaper. This timing is managed by trigger delay.
  - Optimum trigger delay depends on the threshold. 0
- The delay-for-hold can be configured via DAQ software.
  - Inject the signal to row-by-row with signal 0 amplitude of 1.2V
  - Hold scan was performed from the range of 0 20-160 in steps of 20.













## Wafer Delamination











100.0E-3 -50.0E-3 -0.0E+0 -

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 Slab



## Particle Identification



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beam composition (X=COG-Z (cm), Y= Nhits (div by 10)) 19 Raw Hitmap itmap\_beam composition (X+COG-2 (cm), Y= Mets (div by 10))\_19 ≻ 220 -Entries 18436 Mean x 10.53 0 200 Mean y 39.95 Std Dev x 2.312 180 Std Dev y 21.43 - 80 160 140 60 120 100 80 40 60 Electron 20 40 20 0<sup>L</sup>0 20 22 10 12 16 18 2 6 8 14 X<sub>0</sub> Fig. SiWECAL Electron 150 GeV



Fig. AHCAL Pion 200 GeV



## Wafer Delamination



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#### Metrology and PCB Deformation

Setup of a device to measure the flatness of the PCB at different stages
PCBs will be out into cabling machine and dimensions will be monitored before and afterwards

#### Glue – Alternative agents and procedures

After discussion with Astronomy Institute of Paris and Epotek
Test glue of type H20E as alternative to Epotek J2189

Should have higher mechanical stability

Use EPOTEK 301-2 as underfill for mechanical stabilisation (proposal of Epotek)

This underfill has low viscosity that ensures mechanical stability by capillary effect
First tests carried out – Stay tuned for results

Alternative proposal EPOTEK 353ND-T

Epoxy for gluing electrical component, could be used to stabilise glued sensor at sensor boundaries
Data sheet in backup

Further alternatives will be studied

#### Pull tests

•IJCLab will prepare pull tests in order to get a quantitative picture of the mechanical stability

of the glue

•Maybe in combination with C2N – A CNRS Institute specialised for materials





## **New PCB Version**

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## New FE boards

#### Improvements:

- Power distributions
  - Local power regulation
  - Local High Voltage filtering & Supply
- Signal distribution (buffering), data paths
- Monitoring (single ID, temp, probe analogue line)
- ASIC shielding/routing

## Status:

- pre-version 2.0 tested, minor corrections needed
  - Noise uniformity dramatically improved (ex: outliers in thr. / 20 !)
- version 2.1 produced, ... in metrology
  - before cabling, 2<sup>nd</sup> metrology, gluing, ...
  - All material available : ASICs being tested

