

Cooling Examples

SiTracker Meeting
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ALICE:

- small fill factor CMOS design
- 40mW/cm²
- Polyimide cooling pipe
- Water coolant
 - max 500mW/cm²
 - 150mW/cm² cooling power
 - (50mW/cm² for end-of-stave cooling)

ATLAS:

- >350 mW/cm²
- bi-phase CO₂ as coolant
- Ti cooling pipe

ATLASPix3:

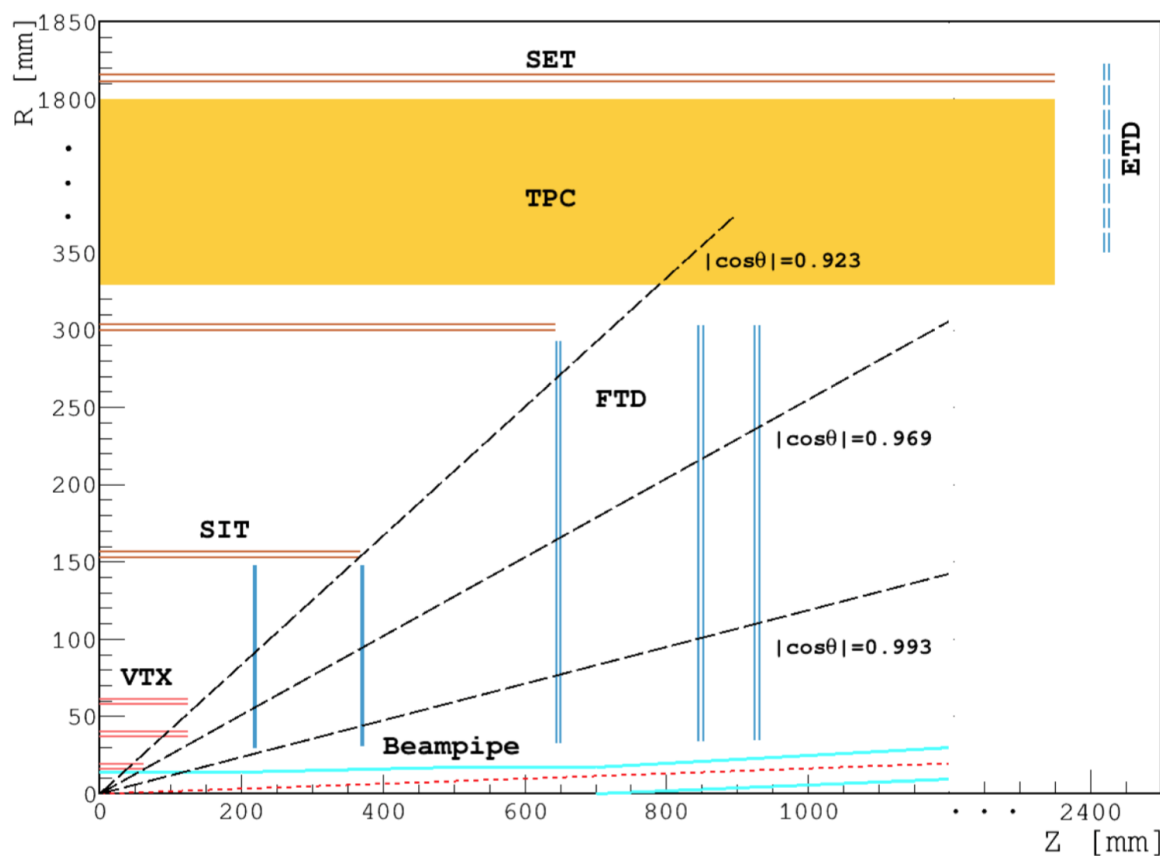
- large fill factor CMOS design
- high efficiency and 25ns demonstrated!

- 140mW/cm² w/o serial powering
 - amplifier + comparator / digital
1.25V*100mA + 1.8V*250mA
 - comparator in NMOS consumes power
 - MuPix uses CMOS in periphery, better in power
 - original estimate was ~340 mW/cm²
 - Caveat: Power estimated at what occupancy?
- Power depends on readout speed and occupancy!
- CMOS can run at high ΔT
- ATLASPix is optimised for radiation hardness. This design choice can be traded against power consumption

Baseline tracker design: TPC

and 3 layers / 5 disks of silicon sensors,

$5.12 + 19.8 \text{ (ETD)} + 53.5 \text{ (SET)} = 78.4 \text{ m}^2$ in CMOS pixels



Detector		Radius R [mm]	$\pm z$ [mm]	Material budget [X_0]
SIT	Layer 1	153	371.3	0.65%
	Layer 2	300	664.9	0.65%
SET	Layer 3	1811	2350	0.65%
FTD		R_{in}	R_{out}	
	Disk 1	39	151.9	220
	Disk 2	49.6	151.9	371.3
	Disk 3	70.1	298.9	644.9
	Disk 4	79.3	309	846
ETD	Disk 5	92.7	309	1057.5
	Disk	419.3	1822.7	2420

Operation mode	H (240)	W (160)	Z (91)
Track multiplicity (BX^{-1})	310	300	32
Bunching spacing (ns)	680	210	25
SIT-L1 occupancy (%)	0.19	0.58	0.52
FTD-D1 occupancy (%)	0.17	0.54	0.48

Table 4.6: Estimated occupancies of the first layers of the SIT (SIT-L1) and the FTD (FTD-D1). See context for more details.

40 - 300 mW/cm² power consumption

**For 5 - 80m² we have to plan with
2 - 32kW of power**

Assumptions: 50mW/cm²; vertex detector;
barrel only; airflow between layers

2.2 - 12 m/s

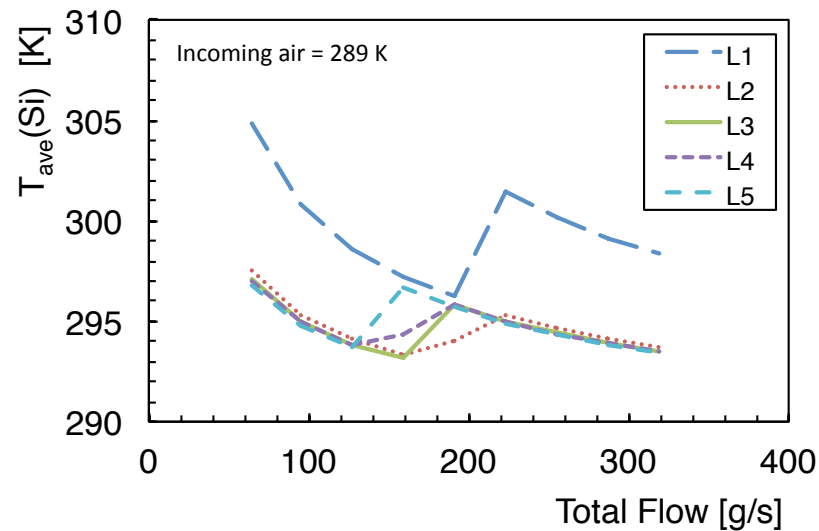
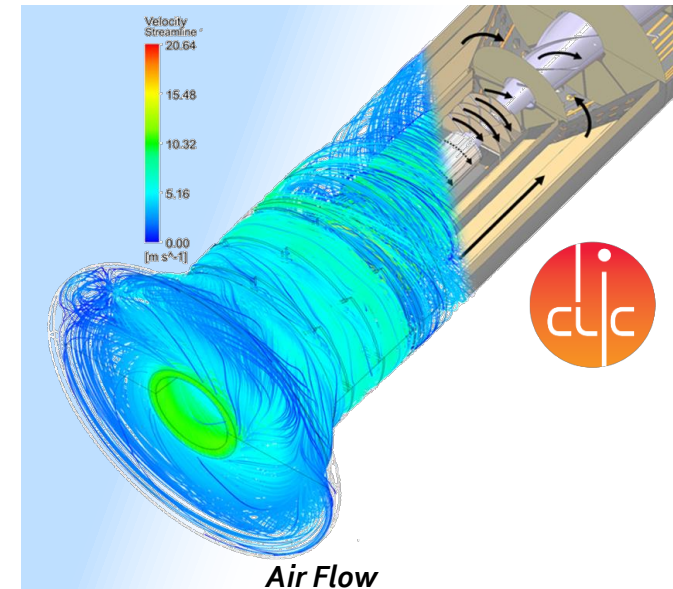


Fig. 4.12: Calculated average temperatures of the five barrel layers of the CLIC_SiD vertex detector as function of the total air-flow rate.

Edge from laminar → turbulent flow



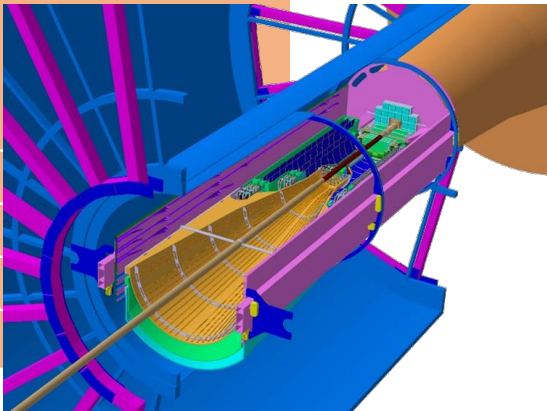
CLIC Air Cooling

One of the main challenges of using gas cooling is to achieve a uniform gas delivery to all detector surfaces whilst minimizing the amount of material in the form of ducts/pipes.

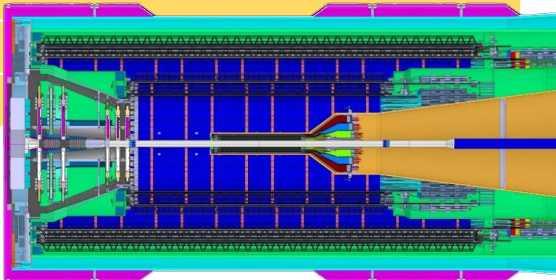
Cooling System Requirements: Air Circulation and Thermal Shield



Requirement	Air Circulation
Flow	$\Phi \sim 150 \text{ m}^3/\text{h}$ <ul style="list-style-type: none">• IB airflow $12 \text{ m}^3/\text{h}$,• OB airflow $75 \text{ m}^3/\text{h}$;• Envelope airflow $25 \text{ m}^3/\text{h}$
Temperature	$T_{\text{out}} = 20^\circ\text{C}$
Humidity	$\text{RH}_{\text{out}} = 35\% \text{ RH}$
Airflow direction	From A side
Air velocity	$< 2 \text{ m/sec}$ in the detector



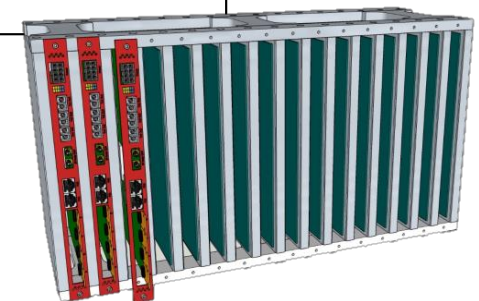
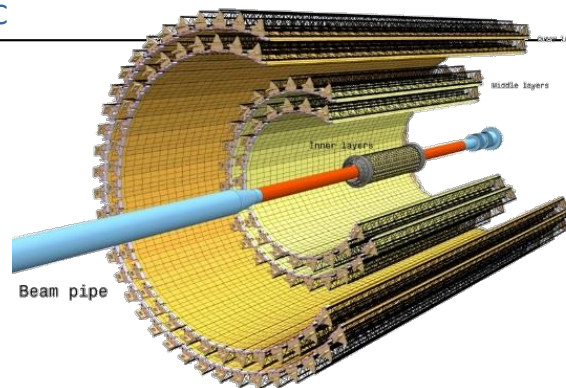
Requirement	Thermal Shield
Thermal	Limit heat exchange between the ITS volume and TPC: provide high thermal resistance to radiation and convection heat transfer
Safety	Comply with Fire safety Instruction/Radiation tolerant
Accessibility	TPC inner bore



Detector and off-Detector Electronics: Specifications



Specification	Detector	Off-Detector Electronics
Power dissipation	<ul style="list-style-type: none"> IB : On-detector 119.55 W*, Bus 0W, Power cable 34W OB-ML : On-detector 1168.99 W*, Bus 26W, Power cable 28W OB-OL : On-detector 3468.96 W*, Bus 135W, Power cable 46W 	<ul style="list-style-type: none"> 10 kW**
Pressure drop stave/board	IB (3l/h)= 0.22 bar OB-ML (5 l/h)= 0.08 bar OB-OL (6.3 l/h)= 0.20 bar	(11l/h) = 0.3 bar
Chip/Board Operative T range	20°C-30°C	20°C-40°C



Remove 14.8kW
 Water Temp range: 18 to 23°C
 n.48 loops
 n.4 controlled manifold:

- Detector IL,
- Detector ML,
- Detector OL
- Off -Detector Electronics

* CHIP Power dissipation (based on Alpid3 3:
 • IB =41 mW/cm²
 • OB= 28 mW/cm²
 and 50% margin

** based on nominal 7.5KW+margin

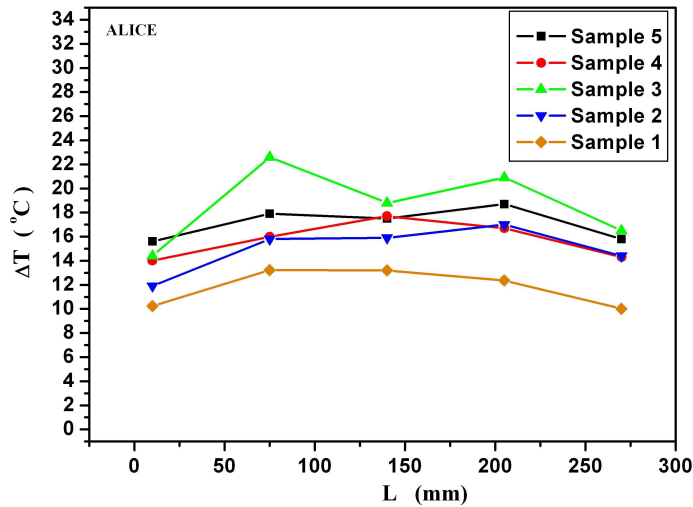


Figure 4. Normalized temperature profiles along the samples (at the central stave line). The vertical axis shows the difference between the temperature of the current point of the sample and the measured temperature of the inlet water. The heating power density was 0.5 W/cm^2 .

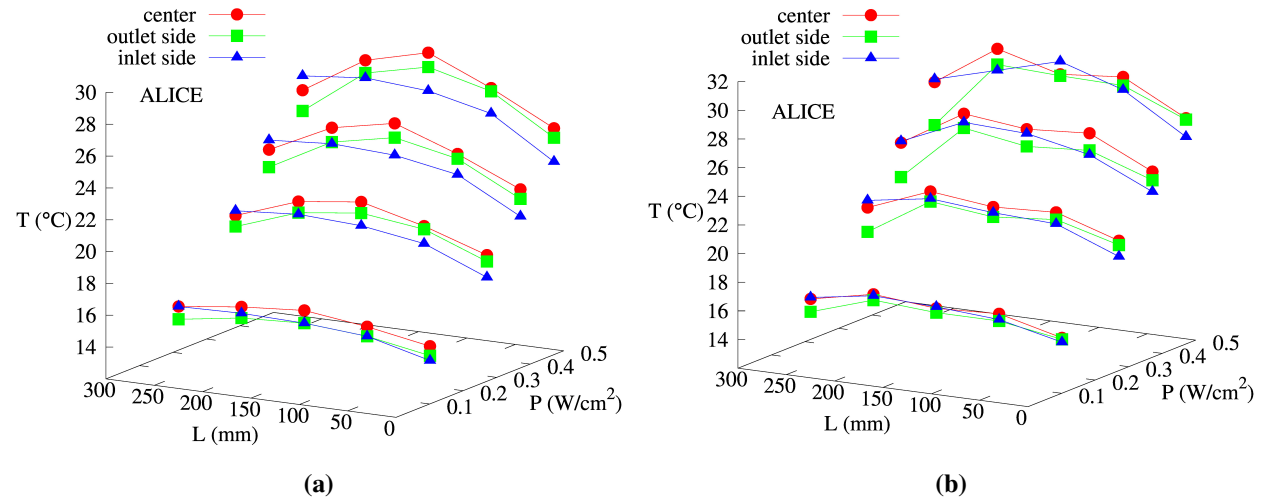
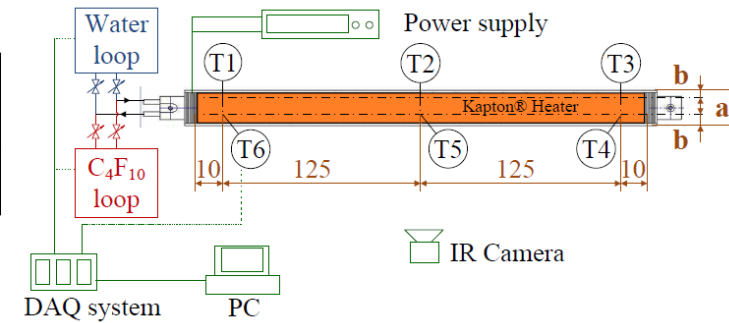


Figure 5. Temperature profile of the sample 4 (a) and sample 5 (b) at: $0.1, 0.3, 0.4$ and 0.5 W/cm^2 power densities. Red dots — thermocouples located in the middle of the heater, green dots — thermocouples located on the edge of the heater close to the inlet channel (temperature 14°C) side, blue dots — thermocouples arranged along the edge of the heater close to outlet water channel.

Thermal characterization



q [W cm^{-2}]	G [L h^{-1}]	$\Delta T_{\text{CHIP-H}_2\text{O}}$ [K]	$\Delta T_{\text{H}_2\text{O}}$ [K]	Δp [bar]	$v_{\text{H}_2\text{O}}$ [m s^{-1}]
0.15	3.0	2.4	1.4	0.3	1.0



Water leakless (<1bar) baseline

Water in 15°C ---> $T_{\text{chip}} < 30^\circ\text{C}$

Pixel max temperature non-uniformity $< 5^\circ\text{C}$

Pressure drop ΔP below 0.3 bar

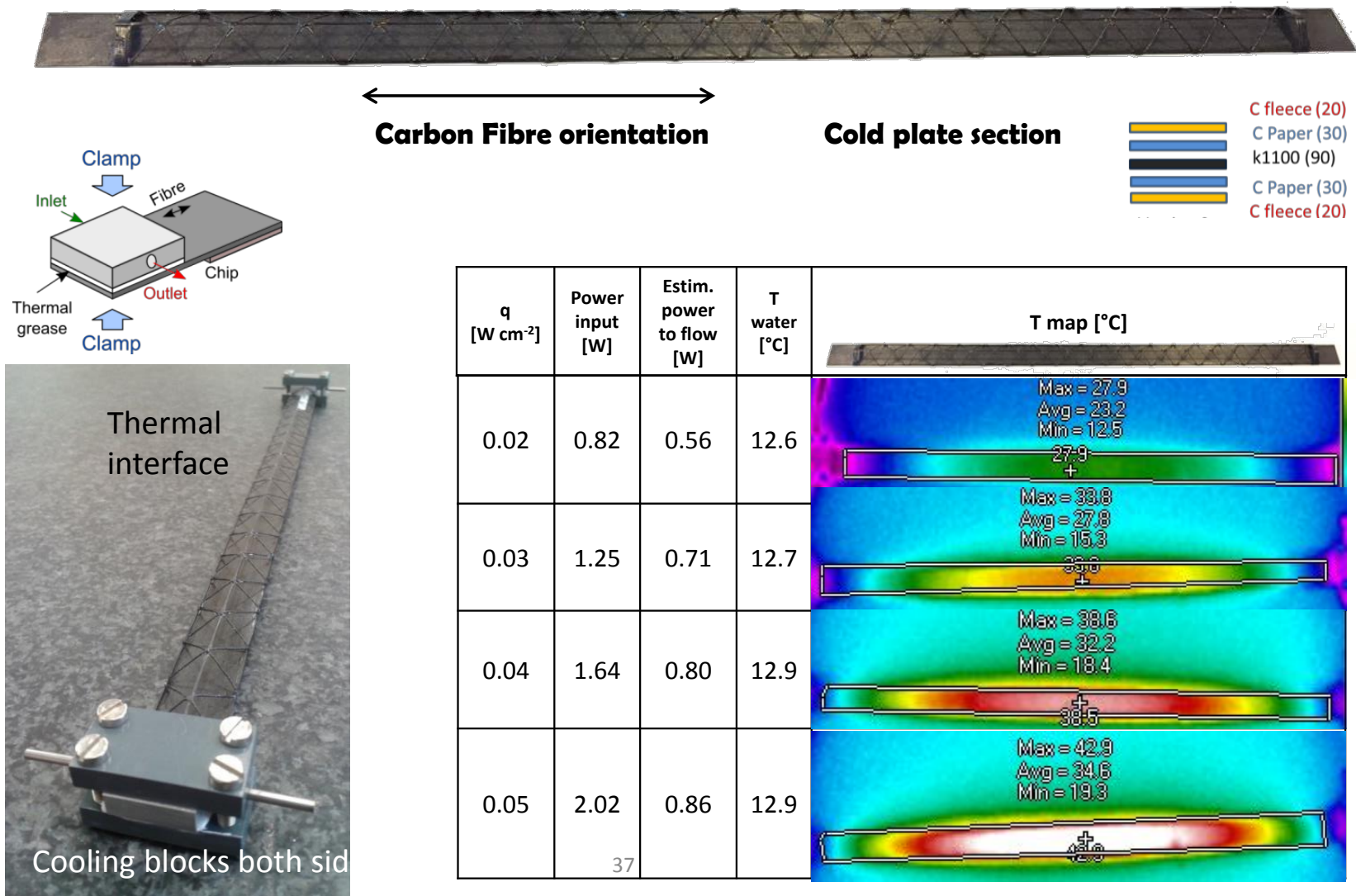


q [W cm^{-2}]	G [L h^{-1}]	$\Delta T_{\text{CHIP-H}_2\text{O}}$ [K]	$\Delta T_{\text{H}_2\text{O}}$ [K]	Δp [bar]	$\Delta T_{\text{HEATERS}}$ [K]	$v_{\text{H}_2\text{O}}$ [m s^{-1}]
0.15	6.3	6.7	6.9	0.08	4	0.31

Alice: Water Cooling, end of stave

Alice:

studies: peripheral cooling, no pipes on the stave



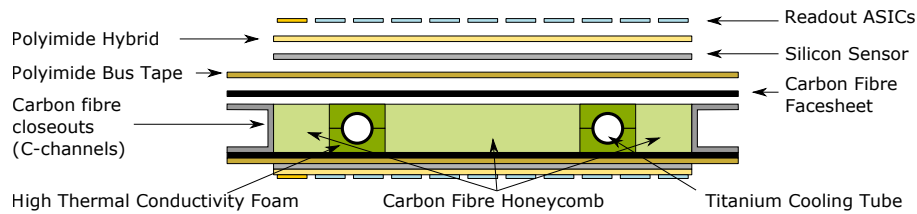


Figure 9.1: Schematic of the internal structure of the stave core, with the silicon sensors and ASICs added. Glue layers are not shown. Not to scale.

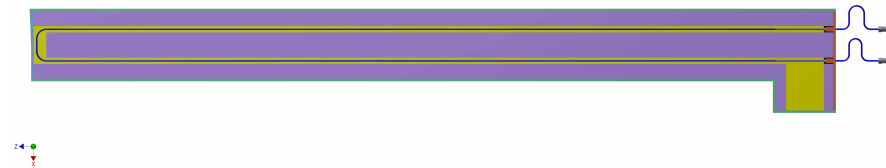


Figure 9.2: A stave core as seen from the top side. Violet colour indicates carbon-fibre honeycomb material and yellow indicates carbon-foam used in the assembly.

Table 9.5: Thermal properties used as input to the FEA.

Part or Interface	Material	$K_x/K_y/K_z$ [W/(m × K)]	Thickness [mm]	Comment
ASIC	Silicon	191 (250K) - 148 (300K)	0.30	
ABC130 to Hybrid	UV cure glue	0.5	0.08	50% coverage
HCC130 to Hybrid	UV cure glue or silver epoxy	0.5 or 3.0	0.08	75% coverage
Hybrid PCB	Cu/polyimide	72/ 0.23 (0.54)* / 72	0.2	*in via region
Power PCB	Cu/polyimide	see text	0.3	
PCB to sensor	FH5313 Epolite	0.23	0.12	75% coverage
Sensor	Silicon	191(250K) - 148(300K)	0.3	
Sensor to Bus	DC SE4445	2.0	0.1 - 0.2	100% coverage
Bus tape	Polyimide/ Cu/Al	0.17 / 0.24 / 0.17	0.17	
Bus to facing	-	(idealised)	-	co-cured
CFRP Facing	0-90-0 CFRP	90 / 1/ 180	0.15	K13C2U fibre, 45 g/m ²
Facing to Foam	Hysol 9396 + graphite powder	1.0	0.1	
Graphite Foam	Allcomp, 2g.cm-3	30	5 mm (core)	
Foam to Pipe	Hysol 9396 + graphite powder	1.0	0.1	
Cooling Pipe	Titanium (grade 2)	16.4	0.14-0.15 (wall)	2 mm inner dia.
Fluid film	Bi-phase CO ₂	htc 4.5 - 17 [kW/m ² K]		

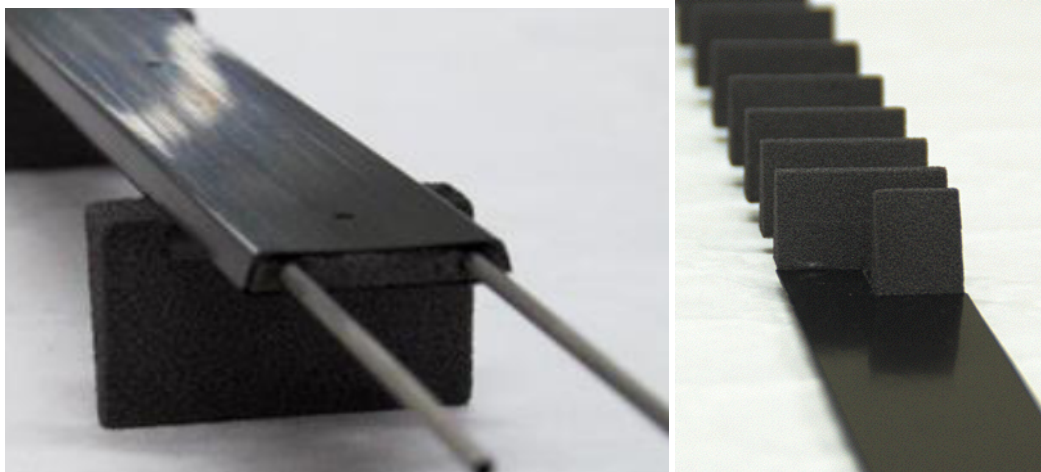
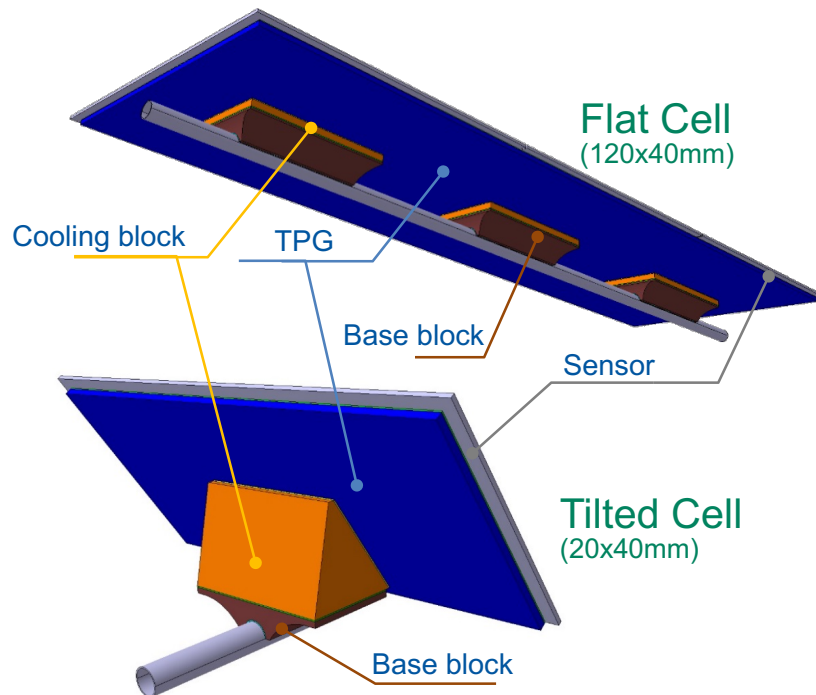
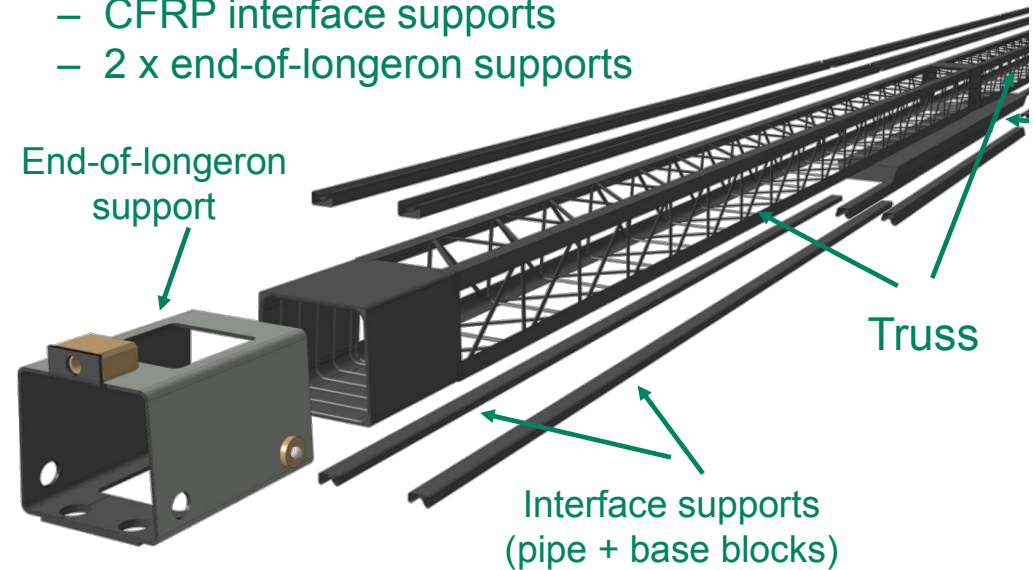


Figure 15.19: ALPINE design for the inclined layout.

- 2 x TRUSS + Central sandwich step
- CFRP interface supports
- 2 x end-of-longeron supports



Layer	TFM ($\text{K} \cdot \text{cm}^2 \cdot \text{W}^{-1}$)		
	Conductive	Convective	Global
0	11	4.3	15
1	16	8.6	25
2	12	8.6	21
3	15	8.6	24
4	18	8.6	27

D. GIUGNI



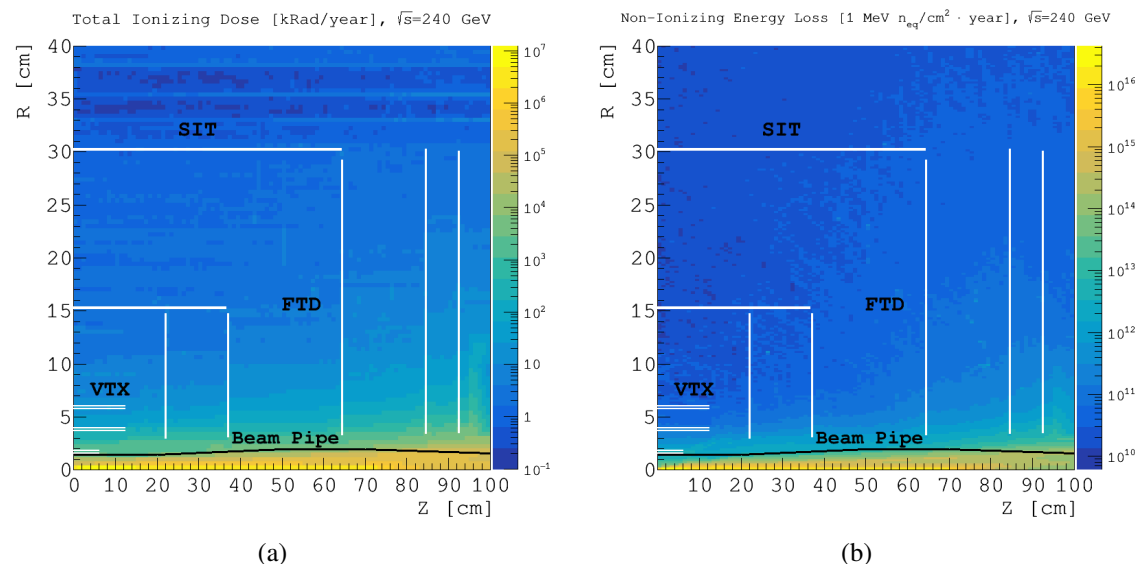


Figure 9.8: Total ionizing dose (TID) and non-ionizing energy loss (NIEL) distribution in $r - z$ for the machine operation at $\sqrt{s} = 240$ GeV. The white lines indicate the locations of the vertex detector (VTX), the forward tracking disks (FTD) and the silicon inner tracker (SIT).

	H (240)	W (160)	Z (91)
Hit Density [hits/cm ² ·BX]	2.4	2.3	0.25
TID [MRad/year]	0.93	2.9	3.4
NIEL [10^{12} 1 MeV n_{eq} /cm ² ·year]	2.1	5.5	6.2

Table 9.4: Summary of hit density, total ionizing dose (TID) and non-ionizing energy loss (NIEL) with combined contributions from pair production and off-energy beam particles, at the first vertex detector layer ($r = 1.6$ cm) at different machine operation energies of $\sqrt{s} = 240, 160$ and 91 GeV, respectively.

Microchannel Cooling:

preferred option: spread out, low X_0 , nice project

embedded in carbon fibre

possible over a long distance?

Ti-pipe cooling:

from X_0 point of view actually not as prohibitive as one might think

lots of UK investment

we know how to do it

can absorb any kind of heat

Water cooling:

highly focussed X_0

water is not a good substance

easy to use

commercial product/fitting

Conclusion:

carefully check X_0 (distribution) for Ti and ITS options

R&D programme for Microchannel cooling