



PARTNERSHIP FOR ADVANCED COMPUTING IN EUROPE

HPC Training Perspectives and Collaborations

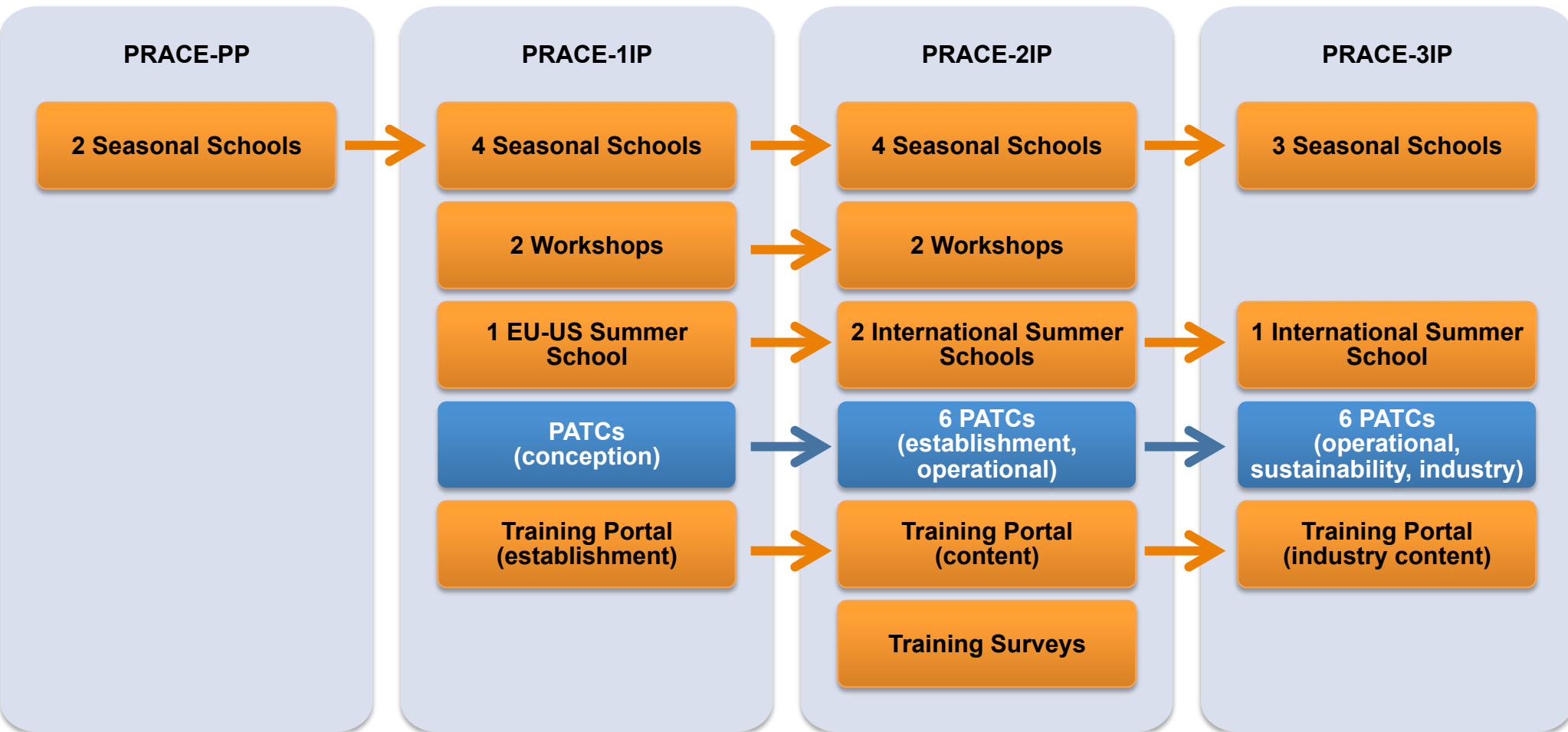
PRACE Advanced Training Centres



PARTNERSHIP
FOR ADVANCED COMPUTING
IN EUROPE



In the context of overall PRACE training...



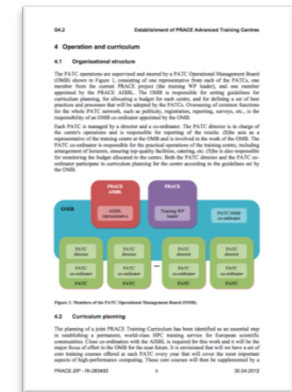
In the context of overall PRACE training...



PRACE Advanced Training Centres

6 PRACE Advanced Training Centres (PATCs): Hubs for world-class HPC training for researchers in Europe.

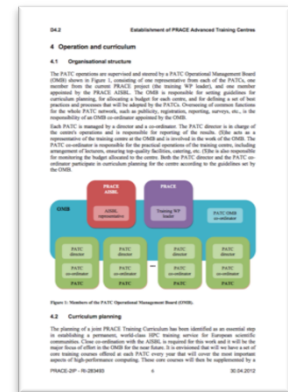
- Barcelona Supercomputing Center (Spain)
 - **Maria-Ribera Sancho** (director)
 - Nia Alexandrov (coordinator)
- CINECA - Consorzio Interuniversitario (Italy)
 - **Nico Sanna** (director)
 - Francesco Falciano (coordinator)
- CSC - IT Center for Science Ltd (Finland)
 - **Martti Louhivuori** (director)
 - Minna Hahl (coordinator)



D4.2

PRACE Advanced Training Centres

- EPCC at the University of Edinburgh (UK)
 - **David Henty** (director)
 - Irina Nazarova (coordinator)
- Gauss Centre for Supercomputing (Germany)
 - **Rolf Rabenseifner** (director)
 - Sabine Höfler-Thierfeldt (coordinator)
- Maison de la Simulation (France)
 - **Edouard Audit** (director)
 - Michel Kern (coordinator)



D4.2

Definitions

Syllabus

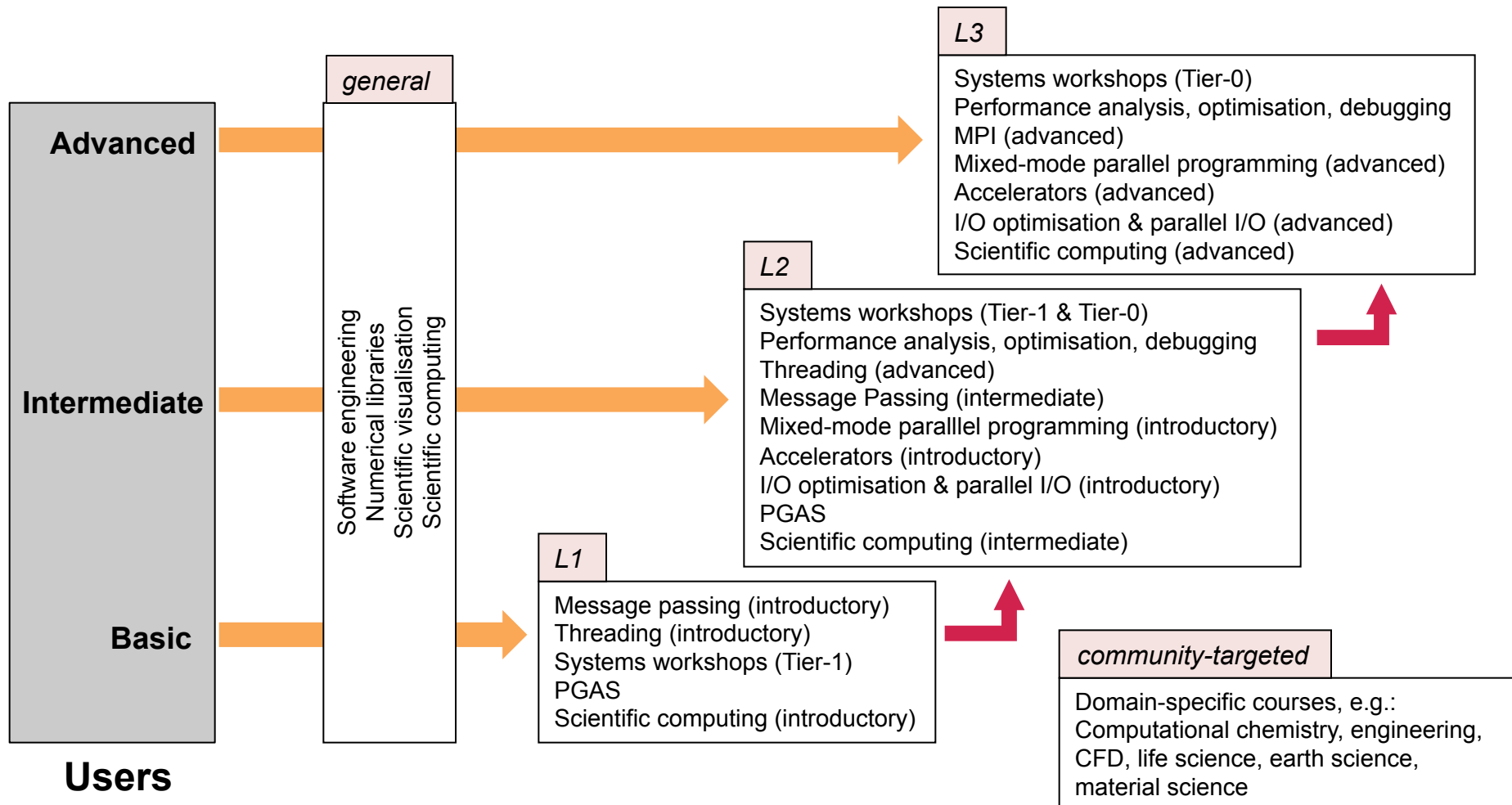
list of subject areas, or individual courses here

Curriculum

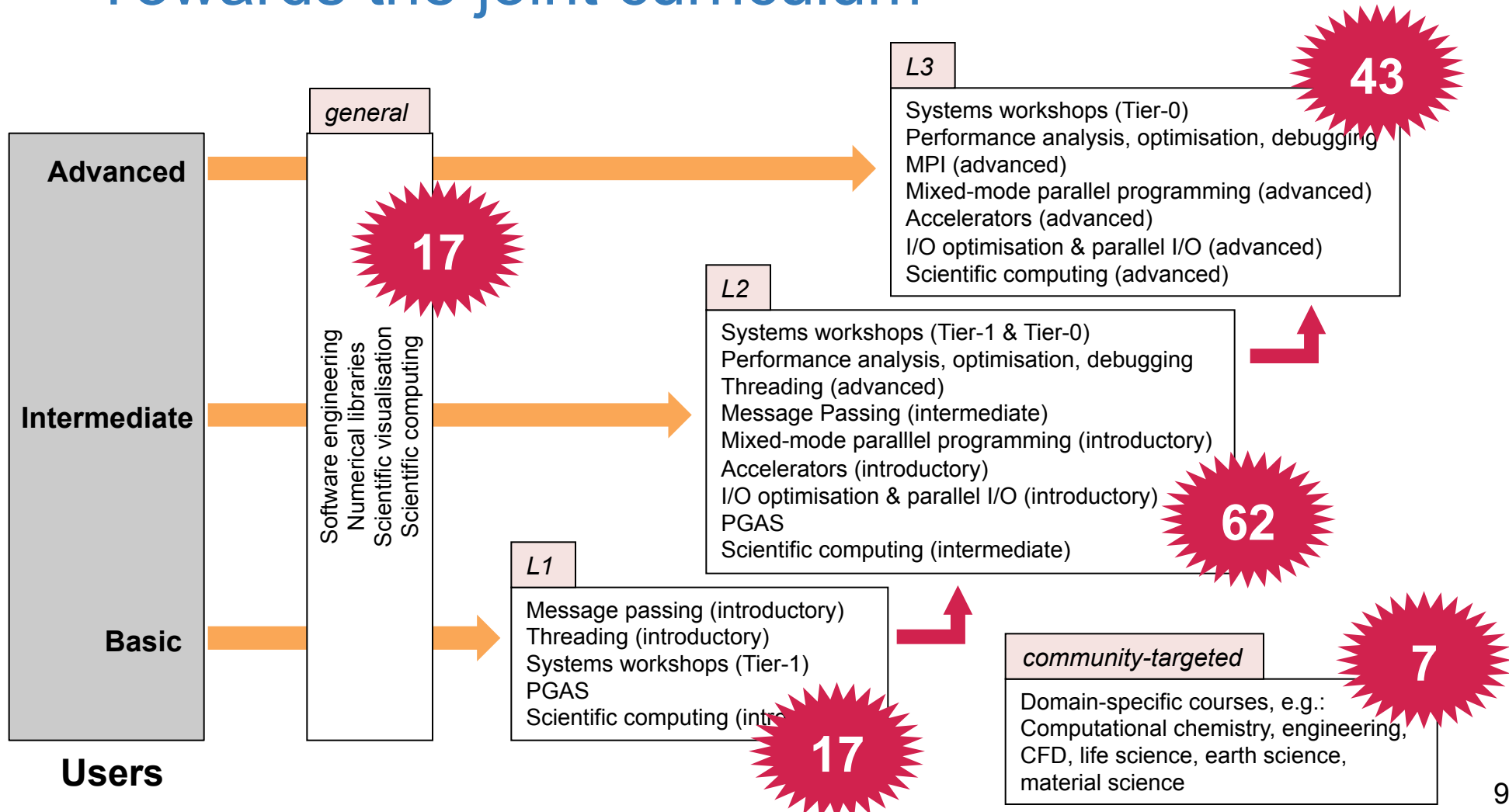
an aggregate of courses; “prescribed learning paths”

Target a **joint** PATC curriculum that caters for researchers with varying levels of HPC experience, along with domain-specific and industry-oriented courses.

Towards the joint curriculum



Towards the joint curriculum



Summary of work so far

- Have run 90 PATC training courses
- 2,000 attendees from all over Europe (and beyond)
- Taken together the joint curriculum covers:
 - key subjects of importance
 - different target audience (HPC experience), including focus on intermediate and advanced users
- Course material (slides, videos etc.) centrally collated

Coherent information, registration and evaluation

www.training.prace-ri.eu



OpenACC Programming for Parallel Accelerated Supercomputers -- an alternative to CUDA from Cray perspective @ HLRS

29-30 April 2013 *Stuttgart (Germany)*
CET timezone

Overview

Registration

Registration Form

Evaluation

Evaluation Form

Local organizer: Rolf
Rabenseifner

This workshop will cover the programming environment of the Cray XK7 hybrid supercomputer, which combines multicore CPUs with GPU accelerators (<http://www.cray.com/Products/Computing/XK7.aspx>). Attendees will learn about the directive-based OpenACC programming model (<http://www.openacc-standard.org>), whose multi-vendor support allows users to portably develop applications for parallel accelerated supercomputers.

The workshop will also demonstrate how to use the Cray Programming Environment tools to identify CPU application bottlenecks, facilitate the OpenACC porting, provide accelerated performance feedback and to tune the ported applications. The Cray scientific libraries for accelerators will be presented, and interoperability of OpenACC directives with these and with CUDA will be demonstrated. Through application case studies and tutorials, users will gain direct experience of using OpenACC directives in realistic applications.



Success Story: Mar 2012 – July 2013

90 courses

260 days of training

6417 participant-days

8.5 / 10

- Average overall course rating, from 1037 feedback forms from 77 courses

Other HPC training

- National
 - e.g. ARCHER training (from EPCC), 72 days p.a. around UK
 - EPSRC-funded HPC Autumn Academy (Cambridge)
- Postgraduate Taught
 - EPCC's MSc in High Performance Computing
 - New MSc in HPC with Data Science
- Within other programmes
 - recently funded CDTs, upcoming European Training Networks
 - relatively modest funding available here

Challenges

- Trainee mobility
- Training the trainers
- Sustainability
- Industrial involvement
- Scope: only “Advanced” courses?
- Scope: training beyond the main curriculum?